Electronic Acknowledgement Receipt					
EFS ID:	5298668				
Application Number:	10755042				
International Application Number:					
Confirmation Number:	8665				
Title of Invention:	Integrated chip package structure using silicon substrate and method of manufacturing the same				
First Named Inventor/Applicant Name:	Mou-Shiung Lin				
Customer Number:	27765				
Filer:	Winston Hsu				
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Time Stamp:	04:17:12				
Application Type:	Utility under 35 USC 111(a)				

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## File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /₊zip	Pages (if appl.)
Information Disclosure Statement (IDS) Filed (SB/08)		618746.PDF	738682	no	4
	0.07.1011.07	5452bf5c1326fe127eab04fe5781f5d96b85 2c84		, 	

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5 NPL Documents		076e0f599ecf871d34e3dba0dc38cad0f4c7 a36e			
	NPL Documents	NPL3.pdf	725052	no	4
Information:			T T	Т	
Warnings:					
4 NPL Documents		a1990a68fa3e1e621fa33a6b7ffcec77afc6d b2c			
	NPL Documents	NPL2.PDF	1433670	no	7
Information:		T	T T		
Warnings:					
			b84eccd37953b5bf2bd42c518e86a390776 9a92c		
3	NPL Documents	NPL1.PDF	530067	no	3
Information:					
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2 Foreign Reference		a2637e2d063d87f453fef9c158d502bce57b 286c			
	Foreign Reference	TW_531854.pdf	1757971	no	32

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#### New International Application Filed with the USPTO as a Receiving Office

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